

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Nagakura, et al.

Assignee: Vlma Microsystems Corporation

Title: "Segment Buffer Loading In A Deinterlacer"

Serial No.: Unknown

Filed: November 25, 2003

Examiner: Unknown

Group Art Unit: Unknown

Atty. Doc. No.: VIM-004

November 25, 2003

Mail Stop Patent Application
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, §1.97 and §1.98, Applicants bring the documents listed on the enclosed form PTO-1449 to the Examiner's attention in the above-captioned application. Citation of the listed documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant application;
2. a representation that a search has been made; or
3. an admission that the information cited is, or is considered to be, material to patentability as defined in §1.56(b).

Copies of the documents listed on the first page of the form PTO-1449 are not submitted because those documents were cited in a prior application (10/235,628) which is relied upon for an earlier filing date under 35 U.S.C. §120.

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on

Nov. 25, 2003, as

Express Mail No. ER452602418US.

T. Lester Wallace

Lester Wallace Nov. 25, 2003
Signature Date

Respectfully submitted,

Lester Wallace

T. Lester Wallace
Attorney for Applicants
Reg. No. 34,748

U.S. Department of Commerce, Patent and Trademark Office						Application No.: Unknown		
						Filing date: Nov. 25, 2003		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Inventors: Nagakura, et al.		
						Group Art Unit: Unknown		
"Segment Buffer Loading In A Deinterlacer"						Examiner name: Unknown		
Express Mail No.: ER452602418US						Attorney Docket No. VIM-004		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate	
	AA	6,339,434	1/15/02	West et al.	345	667		
	AB	6,252,906	6/26/01	Canfield	375	240.21		
	AC							
	AD							
	AE							
Foreign Patent Documents								
							Translation	
	AF	Document Number	Date	Country	Class	Subclass	Yes	No
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AG	Internet pages from Pixelworks Inc. website www.pixelworksinc.com , first page entitled "PixelWorks Products", 3 pages, printed out 6/27/02.						
	AH	Internet pages from of Genesis Microchip Inc. www.genesis-microchip.com , first page entitled "Genesis Any Source - Any Display - gm6010 Digital TV Controller, 4 pages, downloaded 6/27/02.						
	AI	Trident DPTV-iX Preliminary Product Brief from Trident Microsystems, Inc., 12 pages, dated November 2001.						
	AJ	Genesis FLI2300 Digital Video Format Converter Product Brief from Genesis Microchip Inc., 2 pages, printed out 6/27/02.						
	AK	Genesis gm6010 and gm6015 Product Brief from Genesis Microchip Inc., 2 pages, printed out 6/27/02.						
	AL	U.S. Patent Application Serial No. 10/150,685, atty docket no. 013856-000006, entitled "Distrubuted RAM In A Logic Array", 21 pages (and copy of filing receipt), filed 5/17/02.						
	AM	U.S. Patent Application Serial No. 10/164,455, atty docket no. 013856-000001, entitled "Cell Architecture To Reduce Customization In A Semiconductor Device", 33 pages (and copy of filing receipt), filed 6/6/02.						
	AO	NuCORE SiP-1250 Smart Image Processor product description by NuCORE Technology Inc., 2 pages, printed out 7/23/02.						
Examiner			Date Considered					
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								

U.S. Department of Commerce, Patent and Trademark Office	Application No.: Unknown
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventors: Nagakura, et al.
	Group Art Unit: Unknown
"Segment Buffer Loading In A Deinterlacer"	Examiner name: Unknown
Express Mail No.: ER452602418US	Attorney Docket No. VIM-004

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
	BA	5,027,203					
	BB	5,162,907					
	BC	5,410,356					
	BD	5,428,397					
	BE	5,473,383					
	BF	5,521,644					
	BG	5,682,205					
	BH	5,689,305					
	BI	6,133,957					
	BJ	6,166,773					
	BK	6,348,949					
	BL	6,577,345					
	BM						
	BN						

Foreign Patent Documents

							Translation	
	BO	Document Number	Date	Country	Class	Subclass	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	BP	
	BQ	

Examiner	Date Considered
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